



Note 1: For LabInt32 PCB, manually connect V2A to RC4, V2B to RC5, and V1B to RC6.  
 Note 2: Recommended for use in slot DB1, with an ADC32 board in slot DB2.  
 Note 3: +/- 12V is recommended for J5-4 and J5-5, but +15 and/or -15 are also OK.  
 Note 4: If used in LabInt32 slot DB1, omit R10 on main board and short R6 to route clock signal.  
 Note 5: Can also use AD8221. For AD8429,  $V_0 = V_{in} * (1 + 6k/R_{pot})$ .  
 Note 6: Size is 1.5" x 0.8". To print, use Scale=1.0, x offset = -100, y offset = 1600.

Version 2.1: Revised layout to reduce clock pickup at U5 and U2

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| UConn Physics, E. Eyer              |
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